



Product Features

- Small size
- 30MHz ~ 2655MHz
- High gain
- High linearity
- Higher productivity
- Low cost

Applications

- Low Noise Amplifier for CATV, Satellite
- Cable Modem
- FTTH (G-PON, GE-PON)
- Optical node



Package Type : SOT-89

Description

AE314 is designed as low cost drive amplifiers for many applications including FTTH, CATV System. This MMIC is based on Gallium Arsenide Enhancement Mode pHEMT which shows low current draw and very low noise. The data in this spec sheet is valid only for 75ohm application. 50ohm data is in a separate spec sheet.

Electrical Specifications

PARAMETER	UNIT	MIN	TYP	MAX	CONDITION
Frequency	MHz	30	-	2655	-
Gain	dB	19 17	21 19	-	30 ~ 1000MHz 70 ~ 2655MHz
Gain Flatness	dB	-	0.4	-	30 ~ 1000MHz
Input Return Loss	dB	-	-15	-	-
Output Return Loss	dB	-	-13	-	-
Output IP3	dBm	35	38	-	@ 500MHz/5dBm 2tone
1dB Compression Point	dBm	18	22	-	@ 500MHz
Noise Figure	dB	-	2	3	30MHz ~ 1000MHz
CSO	50 ~ 870MHz	dBc	-	-65	135Channel@20dBmV/Ch (Normal Bias)
CTB		dBc	-	-70	
XMOD		dBc	-	-85	
DC Current	mA	-	100	-	Vdd = 5.0V
	mA	-	140	-	Vdd = 5.0V (Gate Rbias)

Note

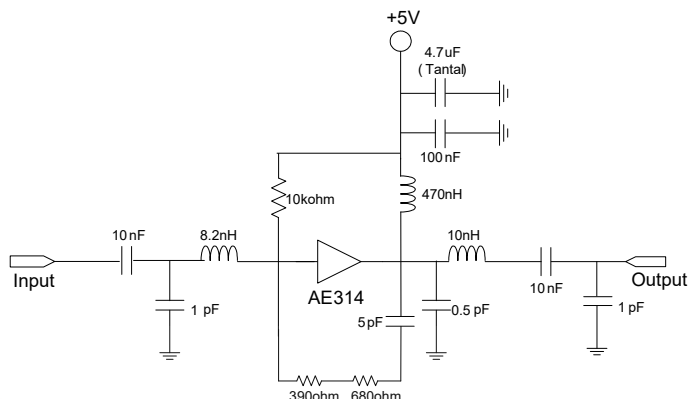
1. Test conditions unless otherwise noted. Test Freq = 500MHz, T=25°C, Vdd=5V, 75Ω system
2. OIP3 measured with 2 tones at an output power of +5dBm/tone separated by 1MHz, Test Freq = 500MHz

Absolute Maximum Ratings

PARAMETER	UNIT	MIN	TYP	MAX
Device Voltage	V	-	5	10
Operating Case Temperature	°C	-40	-	85
Storage Temperature	°C	-40	-	150
ESD Human Body Model	-	-	Class 1A	-
Moisture sensitivity Level	-	-	MSL1	-
Junction temperature	°C	-	-	180
Thermal Resistance (Rth)	°C/W	-	60	-

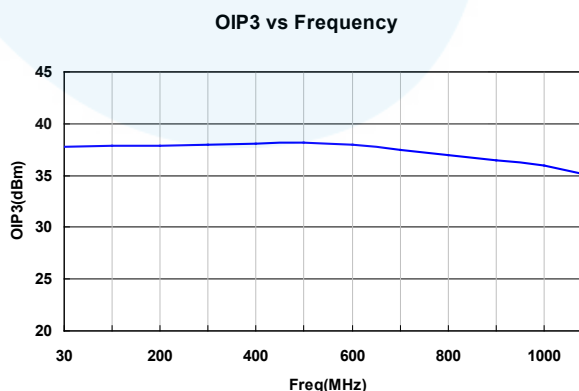
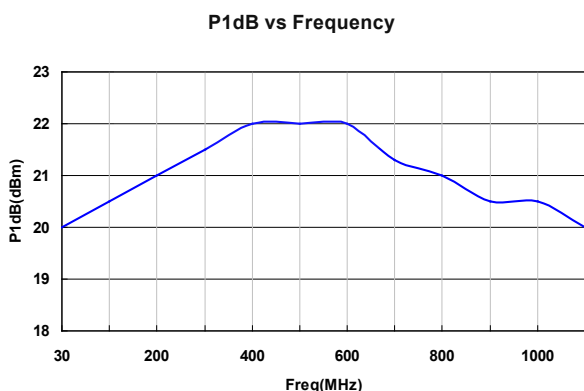
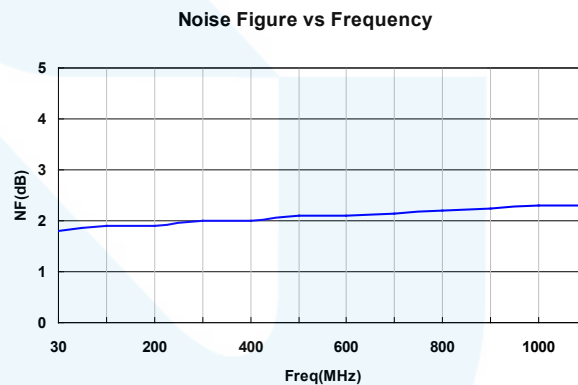
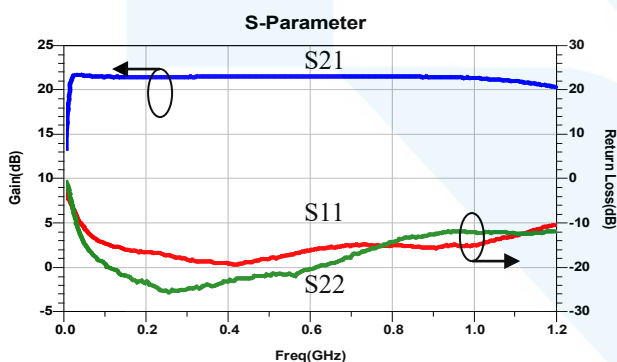
Application Circuit @ 30 ~ 1000MHz, 75ohm System

PARAMETER	UNIT	TYPICAL		
Frequency	MHz	30	500	1000
Gain(S21)	dB	21	21	21
IRL(S11)	dB	-9	-18	-15
ORL(S22)	dB	-10	-21	-12
Output IP3	dBm	37.8	38	35
P1dB	dBm	20	22	19.8
Noise Figure	dB	1.8	2.0	2.3
CSO(1)	dBc	-70		
CTB(1)	dBc	-80		
XMOD(1)	dBc	-85		
Current	mA	140		



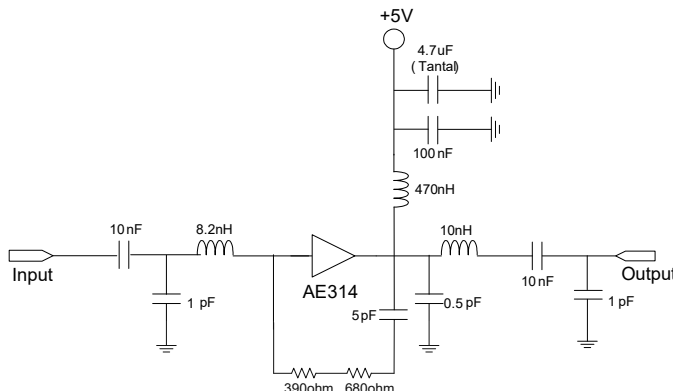
(1) 135channels, +20dBmV/ch

Typical Performance @ VDD=5V, IDS=140mA, T=25°C, 75ohm System



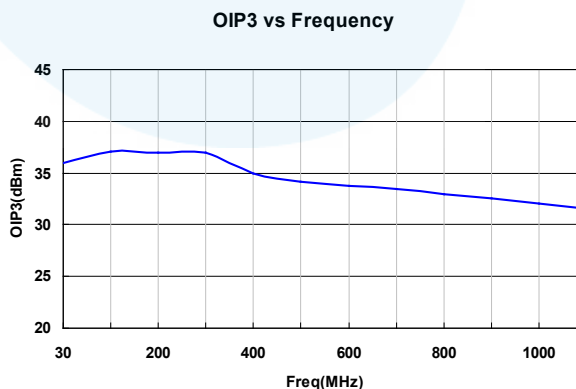
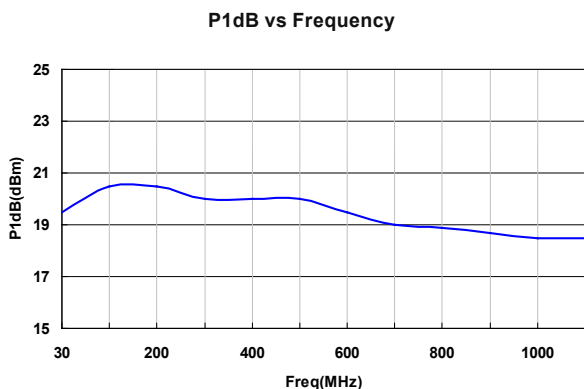
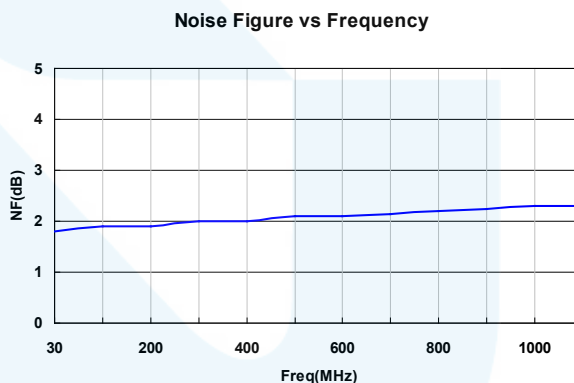
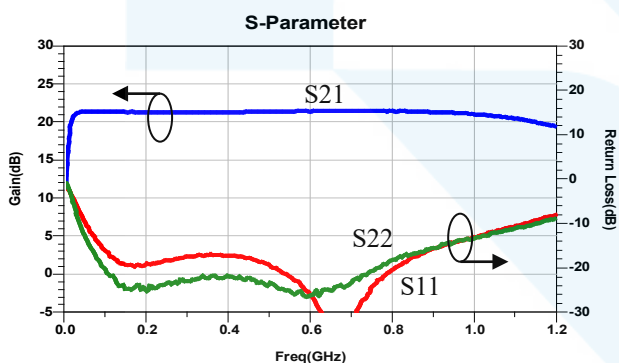
Application Circuit @ 30 ~ 1000MHz, 75ohm System

PARAMETER	UNIT	TYPICAL		
Frequency	MHz	30	500	1000
Gain(S21)	dB	21.6	21.4	21.2
IRL(S11)	dB	-7.5	-20	-13
ORL(S22)	dB	-10	-17	-16.5
Output IP3	dBm	36	34.5	32
P1dB	dBm	19	20	18.5
Noise Figure	dB	1.8	2.0	2.3
CSO(1)	dBc	-65		
CTB(1)	dBc	-70		
XMOD(1)	dBc	-85		
Current	mA	100		



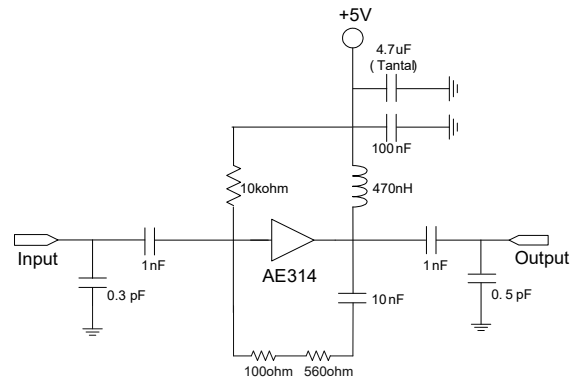
(1) 135channels, +20dBmV/ch

Typical Performance @ VDD=5V, IDS=100mA, T=25°C, 75ohm System



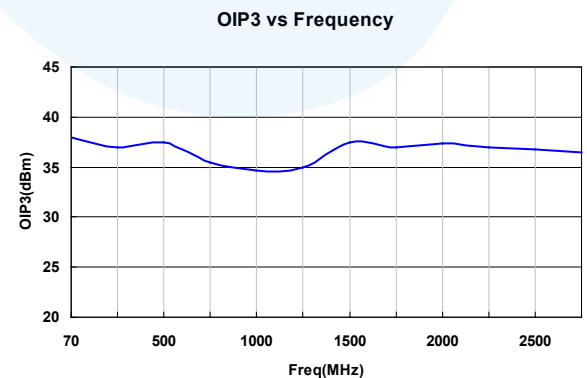
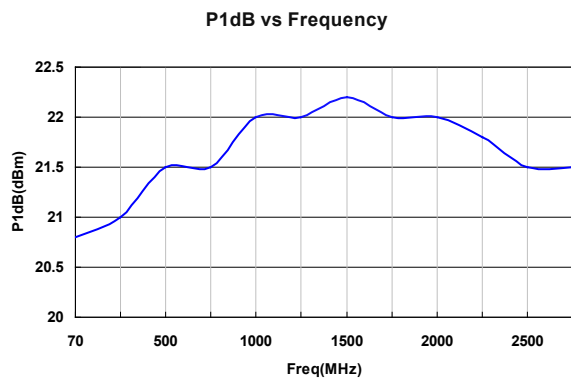
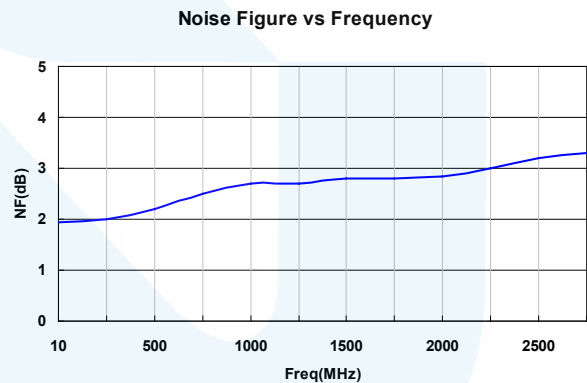
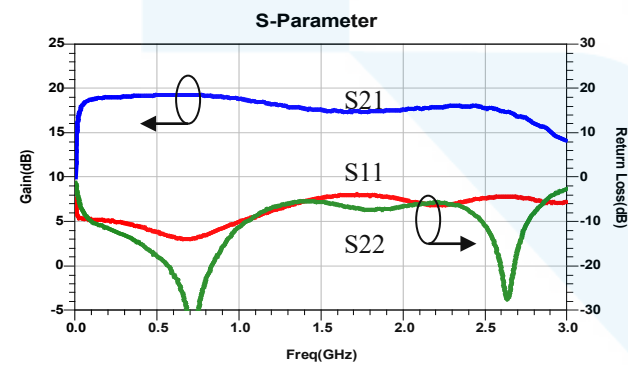
Application Circuit @ 70 ~ 2655MHz, 75ohm System

PARAMETER	UNIT	TYPICAL			
		0.07	1	2	2.6
Frequency	GHz	0.07	1	2	2.6
Gain(S21)	dB	18.6	18.8	17.8	17.4
IRL(S11)	dB	-9.5	-10	-6.2	-4.5
ORL(S22)	dB	-9	-11	-6	-26
Output IP3	dBm	38	35	37.4	36.5
P1dB	dBm	20.8	22	22	21.5
Noise Figure	dB	2	2.7	2.8	3.2
CSO(1)	dBc	-70			
CTB(1)	dBc	-80			
XMOD(1)	dBc	-85			
Current	mA	140			



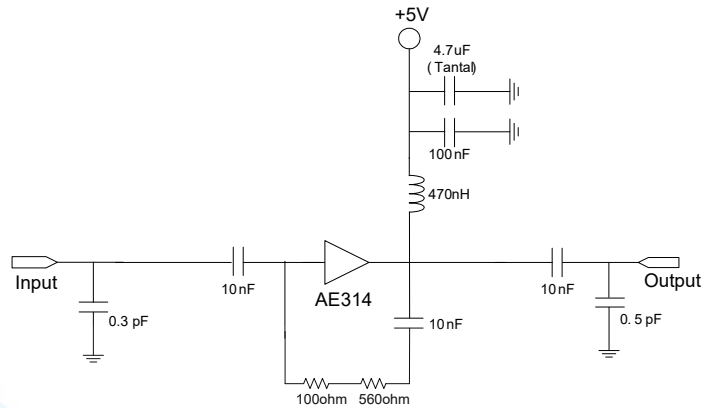
(1) 135channels, +20dBmV/ch

Typical Performance @ VDD=5V, IDS=140mA, T=25°C, 75ohm System



Application Circuit @ 70 ~ 2655MHz, 75ohm System

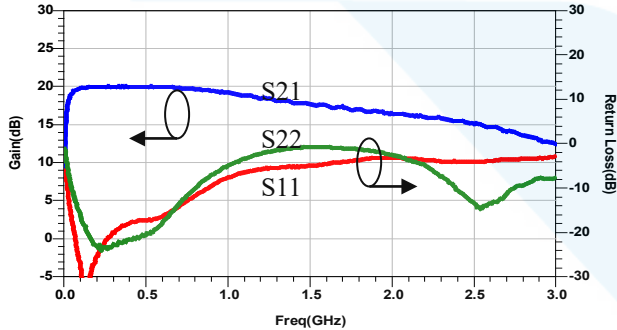
PARAMETER	UNIT	TYPICAL			
Frequency	GHz	0.07	1	2	2.6
Gain(S21)	dB	19.5	18.5	17	15
IRL(S11)	dB	-12	-5	-4	-4
ORL(S22)	dB	-10	-4	-4	-10
Output IP3	dBm	36	31	31	30
P1dB	dBm	19	19	18.5	18
Noise Figure	dB	2.1	2.8	2.9	3.3
CSO(1)	dBc	-65			
CTB(1)	dBc	-70			
XMOD(1)	dBc	-85			
Current	mA	100			



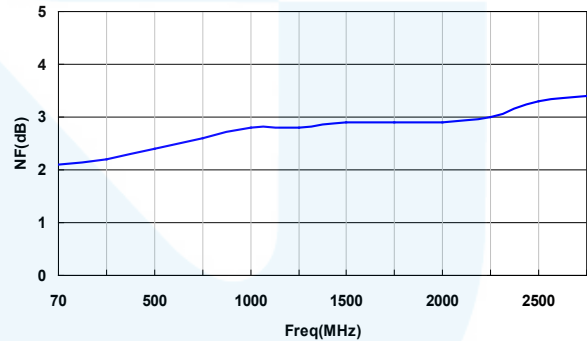
(1) 135channels, +20dBmV/ch

Typical Performance @ VDD=5V, IDS=100mA, T=25°C, 75ohm System

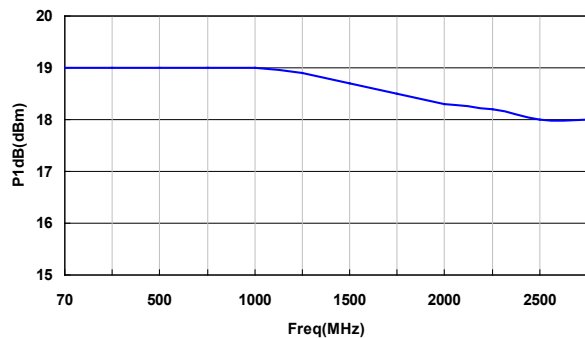
S-Parameter



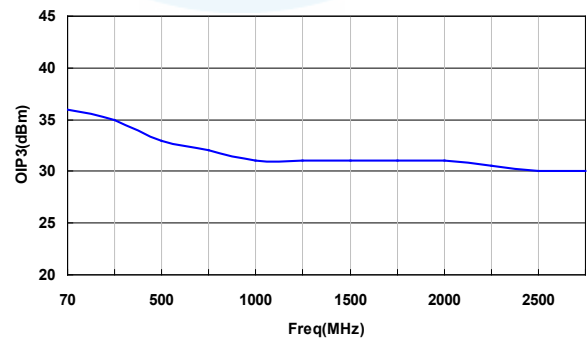
Noise Figure vs Frequency



P1dB vs Frequency

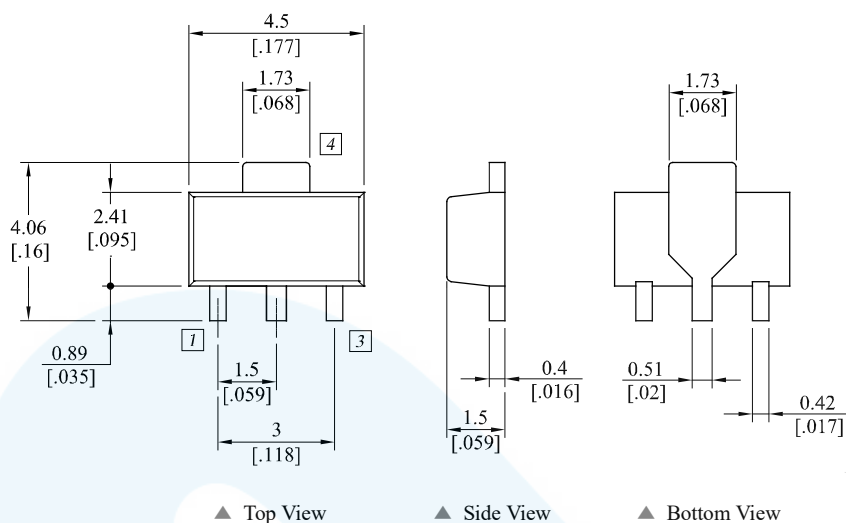


OIP3 vs Frequency



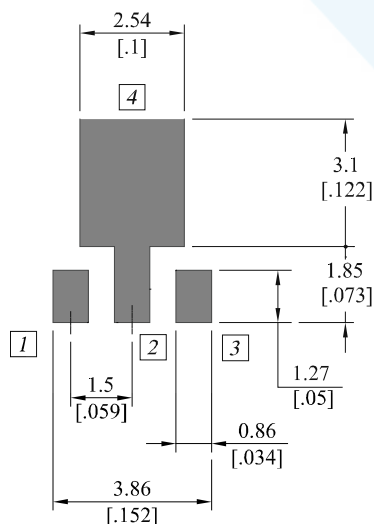
Package Dimensions (Type: SOT-89)

* Unit: mm[inch] | Tolerance ±0.2[.008]

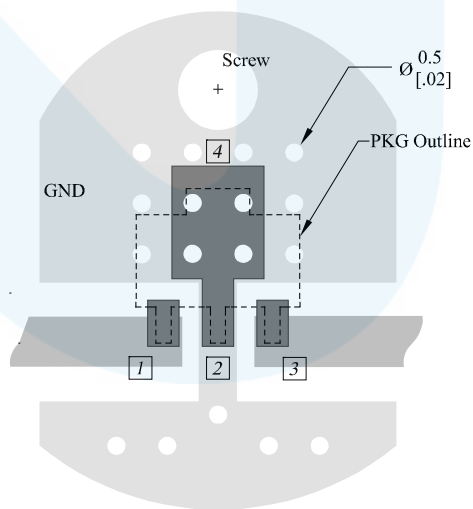


Pin Description			
Pin No	Function	Pin No	Function
1	Input	4	GND
2	GND	-	-
3	Output / Bias	-	-

Recommended Pattern



Recommended Mounting Configuration



* Mounting Configuration Notes

1. Ground / thermal via holes are critical for the proper performance of this device.
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via hole region contacts the heatsink.
4. Do not put solder mask on the backside of the PCB in the region where the board contacts the heatsink.
5. RF trace width depends upon the PCB material and construction.
6. Use 1 oz. Copper minimum.

Revision History

Part Number	Release Date	Version	Modification	Data Sheet Status
AE314	2014.04.22	1.3	Absolute Maximum Ratings (Delete Tj Typ)	-
AE314	2012.08.23	1.2	New datasheet format and Change Gate bias Circuit	-
AE314	2010.04.23	1.1	Change by Current Typical Spec	-



Certification

This product is manufactured by a company that is certified for the AS9100D quality management system.

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