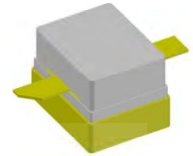


Product Features

- Up to 6GHz
- 55W Saturated Power @ 48V, 2.6GHz
- 60% Drain Efficiency @ Psat, 2.6GHz
- 30% Drain Efficiency @ 41dBm, 2.6GHz

Applications

- WiMAX, LTE, WCDMA, GSM
- Multi-Band, Multi-Mode
- Multi-Carrier
- High Efficiency, Doherty Amplifier



Package Type : NS-CS01

Typical Single-Carrier LTE Performance ($V_{DS} = +48V$, $T_C = 25^\circ C$, 50Ω)

Frequency [MHz]	Peak Power		Average Power ^{*1}			
	Power [W]	Drain Efficiency [%]	Power [W]	Gain [dB]	Drain Efficiency [%]	ACLR [dBc]
2505.0	59.7	65.8	12.6	15.2	32.3	-37.7
2595.0	60.4	67.3	12.6	15.7	34.1	-36.5
2685.0	59.2	70.4	12.6	15.6	36.1	-32.7

Note

*1 Measured in the RT12055P test board amplifier circuit, under LTE 10MHz, PAR 7.5dB @0.01% probability on CCDF.

Absolute Maximum Ratings

Rating	Symbol	Value	Unit	Condition
Drain to Source Voltage	V_{DSS}	150	V	$T_C = 25^\circ C$
Gate to Source Voltage	V_{GS}	-10, +2	V	$T_C = 25^\circ C$
Operating Voltage	V_{DD}	52	V_{DC}	-
Maximum Forward Gate Current	I_{GMAX}	7	mA	$T_C = 25^\circ C$
Maximum Drain Current ^{*1}	I_{DMAX}	3	A	$T_C = 25^\circ C$
Power Dissipation	P_{DISS}	30	W	$T_C = 85^\circ C$
Storage Temperature	T_{STG}	-65, +150	$^\circ C$	-
Case Operating Temperature	T_C	-40, +150	$^\circ C$	30 seconds
Operating Junction Temperature ^{*2}	T_J	225	$^\circ C$	-
Soldering Temperature ^{*3}	T_S	245	$^\circ C$	-

Note

*1 Current Limit for long term, reliable operation.

*2 Continuous use at maximum temperature will affect MTTF.

*3 Refer to the Application Note(AN-002) on soldering - "Solder Condition for RFHIC's GaN Device"

Thermal Characteristics

Rating	Symbol	Value	Unit	Condition
Thermal Resistance, Junction to Case	$R_{\theta JC}$	4.71 ^{*1}	$^\circ C/W$	$T_C = 85^\circ C$

Note

*1 Measured for the RT12055P at dissipation power of 29.7W

Electrical Characteristics (T_c=25°C unless otherwise noted)

Characteristics	Conditions	Symbol	Min	Typ	Max	Unit
DC Characteristics ^{*1}						
Gate Threshold Voltage	V _{DS} = 10V	V _{GS(TH)}	-3.8	-3.0	-2.3	V _{DC}
	I _D = 7.2mA					
Gate Quiescent Voltage	V _{DS} = 48V	V _{GS(Q)}	-	-2.6	-	V _{DC}
	I _D = 200mA					
Saturated Drain Current ^{*2}	V _{DS} = 6V	I _{DS}	5.8	7.0	-	A
	V _{GS} = 2V					
Drain-Source Breakdown Voltage	V _{GS} = -8V	V _{BR}	150	-	-	V
	I _D = 7.2mA					
Gate Leakage Current	V _{GS} = -8V	I _{GLKG}	-1.6	-	-	mA
	V _{DS} = 120V					
Drain Leakage Current	V _{GS} = -8V	I _{DLKG}	-	-	2.9	mA
	V _{DS} = 120V					
RF Characteristics (Fc=2595MHz unless otherwise noted)						
Saturated Output Power ^{*3,6}	V _{DS} = 48V	P _{SAT}	-	55	-	W
	I _{DQ} = 200mA					
Pulsed Drain Efficiency ^{*3}	V _{DS} = 48V	η	55	60	-	%
	I _{DQ} = 200mA					
	P _{OUT} = P _{SAT} Pulsed					
Modulated Gain ^{*4}	V _{DS} = 48V	G _{BR}	13.5	15.0	-	dB
	I _{DQ} = 200mA					
	P _{OUT} = 41dBm					
LTE Linearity ^{*4}	V _{DS} = 48V	ACLR	-	-32.0	-28.0	dBc
	I _{DQ} = 200mA					
	P _{OUT} = 41dBm					
Modulated Drain Efficiency ^{*4}	V _{DS} = 48V	η	28.0	30.0	-	%
	I _{DQ} = 200mA					
	P _{OUT} = 41dBm					
Output Mismatch Stress ^{*3, 5}	V _{DS} = 48V	VSWR	-	-	10:1	ψ
	I _{DQ} = 200mA					
	P _{OUT} = P _{SAT} Pulsed					

Note

*1 Measured on wafer prior to packaging.

*2 Scaled from PCM data.

*3 Pulse width 100μsec, Duty Cycle 10%.

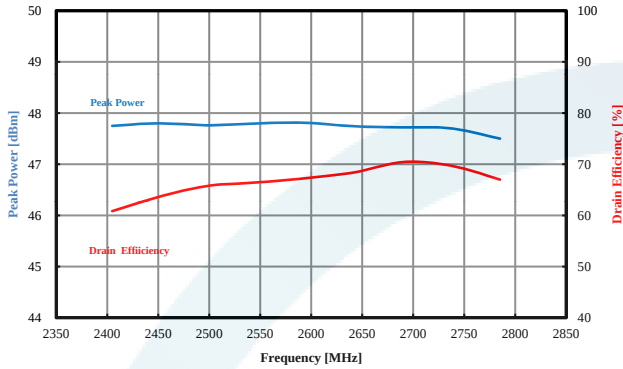
*4 Measured in the RT12055P-2600MHz test board amplifier circuit, under LTE 10MHz, PAR7.5dB @0.01% probability on CCDF.

*5 Measured in the RT12055P-2600MHz test board amplifier circuit. No damage at all phase angles.

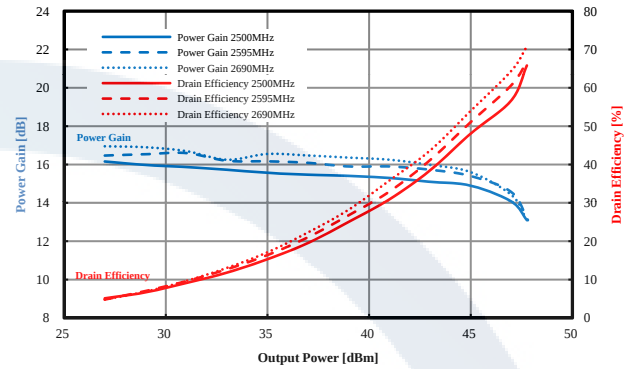
*6 Psat is defined as ΔPout/ΔPin<0.1, where ΔPin is increased input power, ΔPout is increased output power.

Typical Pulsed Signal Performance (Tc=25°C, Measured in the RT12055P-2600MHz test board amplifier circuit)

Peak Power, Drain Efficiency vs. Frequency



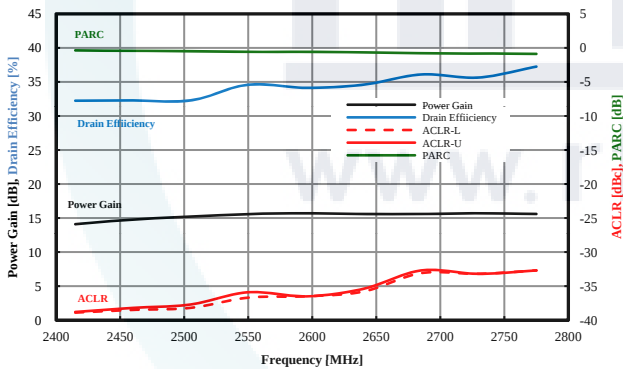
Pulsed Power Gain, Drain Efficiency vs. Output Power



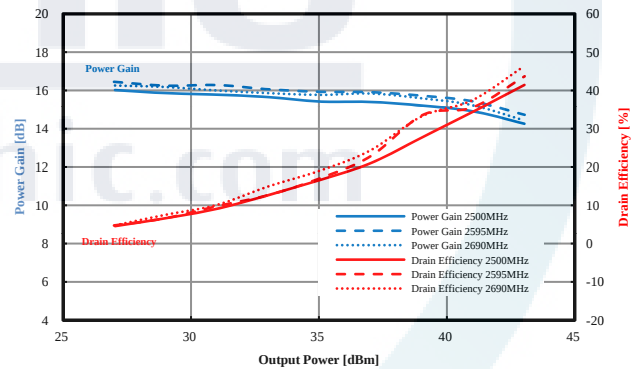
$V_{DS} = 48V$, $I_{DQ} = 200mA$, Pulse Width = 100μsec, Duty Cycle = 10%

Typical LTE Signal Performance (Tc=25°C, Measured in the RT12055P-2600MHz test board amplifier circuit)

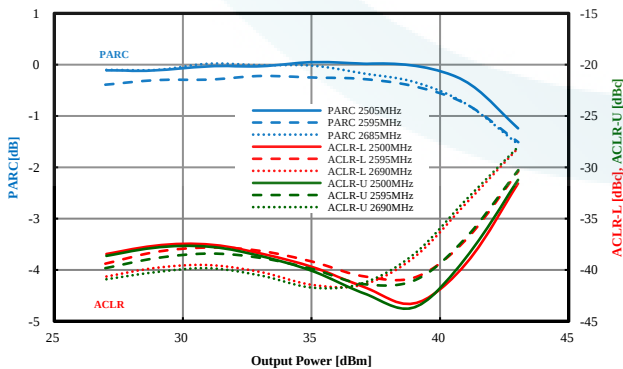
LTE Power Gain, Drain Efficiency, ACLR, PARC vs. Frequency



Power Gain, Drain Efficiency vs. Output Power



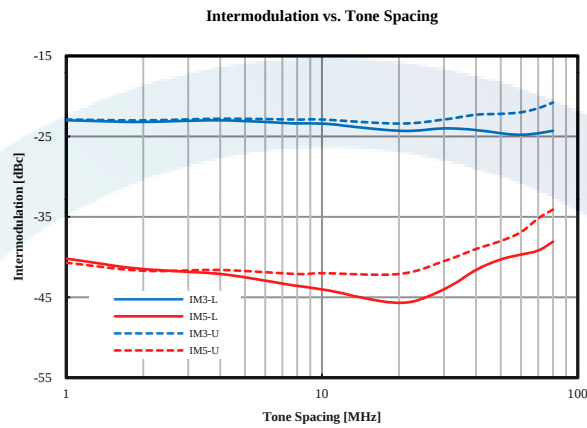
PARC, ACLR vs. Output Power



$P_{AVG} = 41dBm$, $V_{DS} = 48V$, $I_{DQ} = 200mA$
 LTE 10MHz BW, PAPR=7.5dB @ 0.01% Probability on CCDF

Typical 2-tone Intermodulation Imbalance Performance

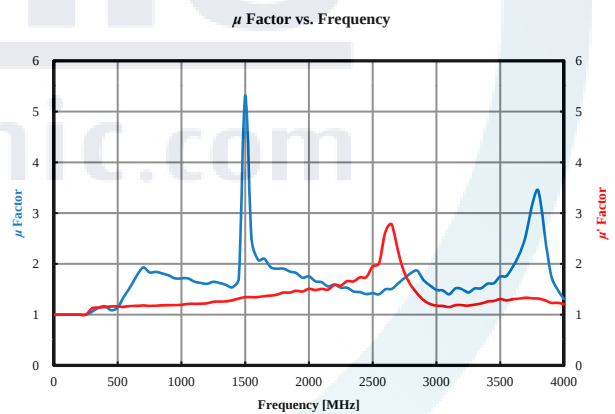
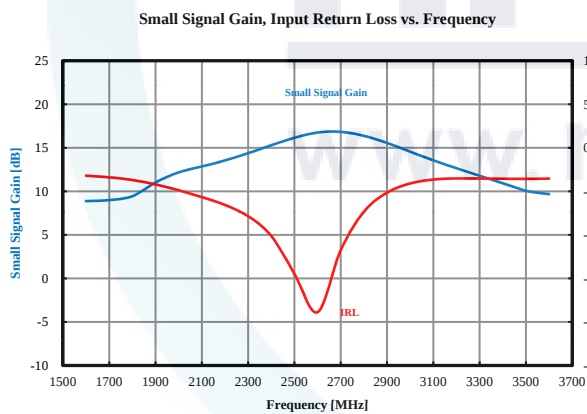
(Tc=25°C, Measured in the RT12055P-2600MHz test board amplifier circuit)



2-tone Power = 45.4dBm, $V_{DS} = 48V$, $I_{DQ} = 200mA$

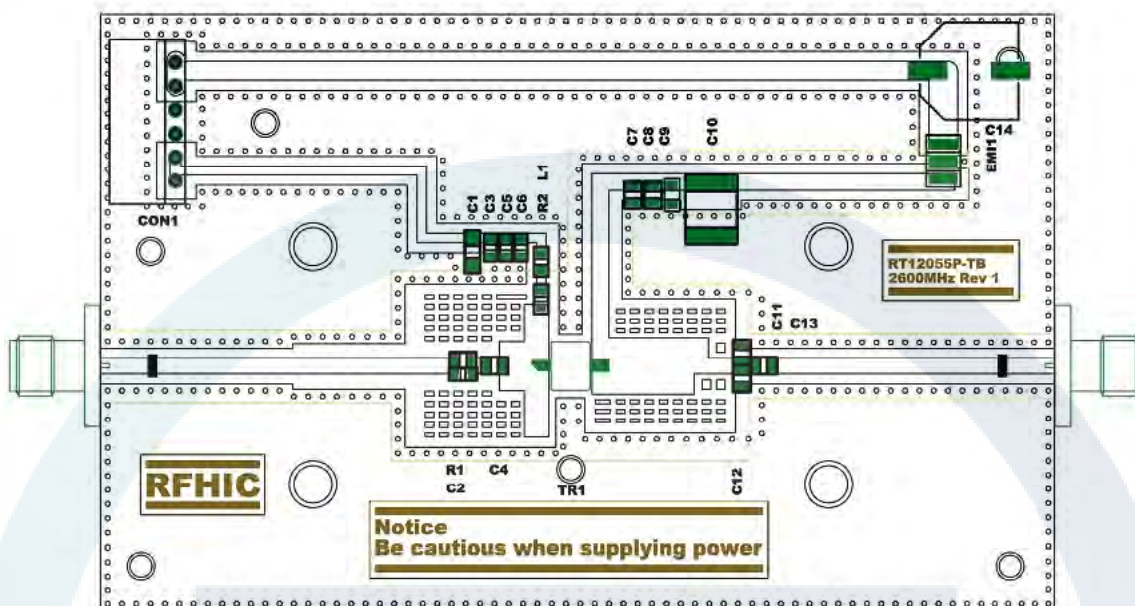
Typical Small Signal Performance

(Tc=25°C, Measured in the RT12055P-2600MHz test board amplifier circuit)



$P_{IN} = 0dBm$, $V_{DS} = 48V$, $I_{DQ} = 200mA$

Test Board Component Layout(2600MHz)

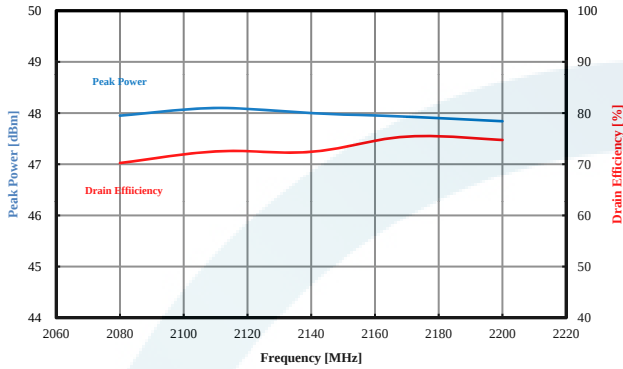


Part	Description	Part Number	Manufacturer
R1	300 ohm Chip Resistor	MCR03EZJH301	ROHM
R2	51 ohm Chip Resistor	MCR10EZJH510	ROHM
L1	22nH Wire Wound Inductor	LQW18AN22NG00	MURATA
C1	Polymer Capacitor	TCJB476M010R0070	AVX
C2	1.5pF High Q Capacitor	201CHA1R5BSLE	TEMEX
C3	1uF Chip Capacitor	GRM188R71H105KA01D	MURATA
C4,C7	10pF High Q Capacitor	201CHA100JSLE	TEMEX
C5	1nF Chip Capacitor	GRM188R71H102KA01D	MURATA
C6	10pF Chip Capacitor	GRM1885C1H100JA01D	MURATA
C8	100pF High Q Capacitor	201CHA101JSLE	TEMEX
C9	0.1uF Chip Capacitor	C2012X7R2A104K	TDK
C10	10uF MLCC	RS80R2A106M	MARUWA
C11	-	-	-
C12	0.5pF High Q Capacitor	201CHA0R5BSLE	TEMEX
C13	3.9pF High Q Capacitor	201CHA3R9BSLE	TEMEX
C14	33uF Aluminum Capacitor	MKV100VCJ10M	SAMYOUNG
EMI1	1000pF EMI Filter	CTH32R102S20A	MARUWA
CON1	DC Connector	5268-06A	MOLEX
PCB	$\epsilon_r=3.66 \pm 0.05$, 0.030" (0.762mm)	RO4350B	ROGERS Corp.
TR1	55W GaN Transistor	RT12055P	RFHIC

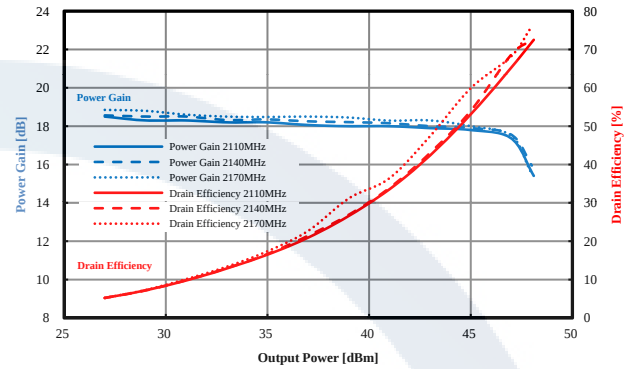
Typical Pulsed Signal Performance

(T_c=25°C, Measured in the RT12055P-2100MHz test board amplifier circuit)

Peak Power, Drain Efficiency vs. Frequency



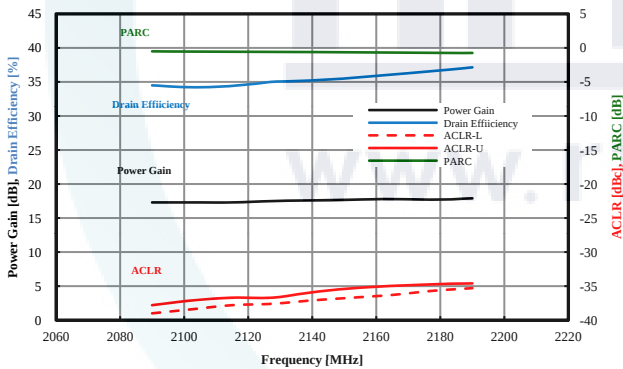
Pulsed Power Gain, Drain Efficiency vs. Output Power


 $V_{DS} = 48V, \quad I_{DQ} = 200mA, \text{ Pulse Width} = 100\mu\text{sec}, \text{ Duty Cycle} = 10\%$

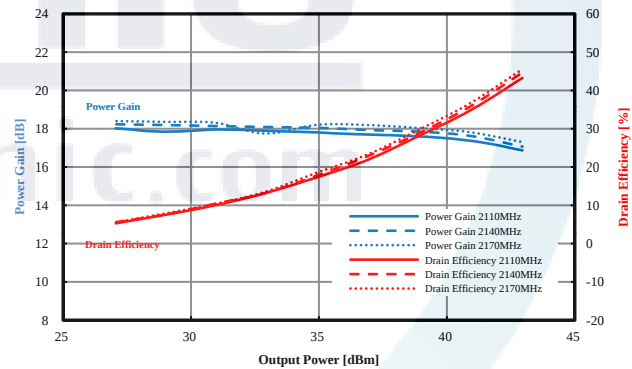
Typical LTE Signal Performance

(T_c=25°C, Measured in the RT12055P-2100MHz test board amplifier circuit)

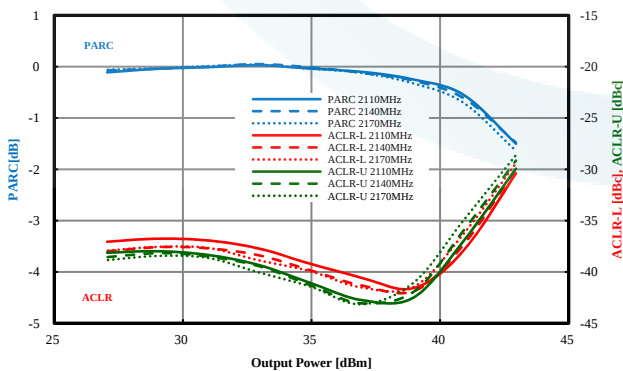
LTE Power Gain, Drain Efficiency, ACLR, PARC vs. Frequency



Power Gain, Drain Efficiency vs. Output Power

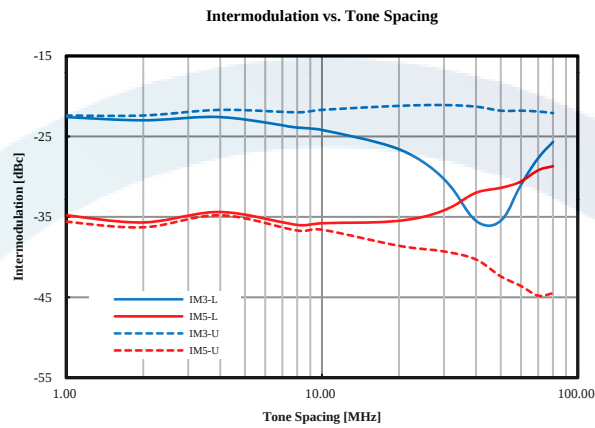


PARC, ACLR vs. Output Power


 $P_{AVG} = 41dBm, \quad V_{DS} = 48V, \quad I_{DQ} = 200mA$
 LTE 10MHz BW, PAPR=7.5dB @ 0.01% Probability on CCDF

Typical 2-tone Intermodulation Imbalance Performance

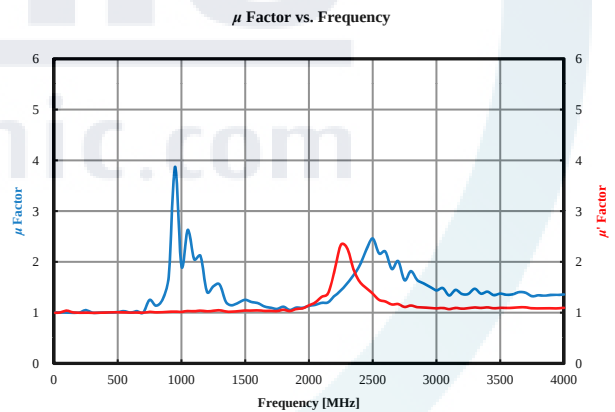
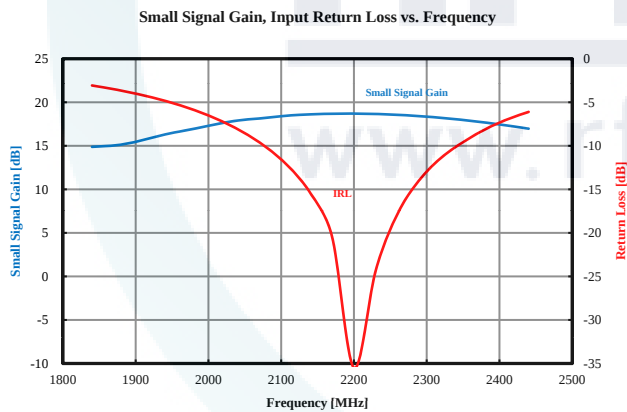
($T_c=25^\circ\text{C}$, Measured in the RT12055P-2100MHz test board amplifier circuit)



2-tone Power = 45.4dBm, $V_{DS} = 48\text{V}$, $I_{DQ} = 200\text{mA}$

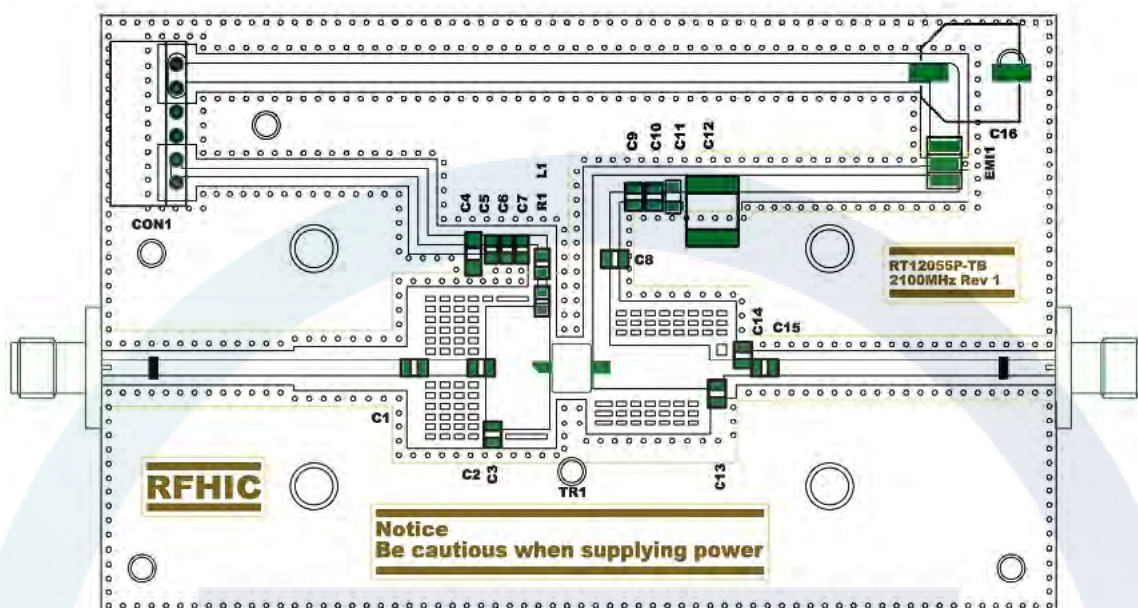
Typical Small Signal Performance

($T_c=25^\circ\text{C}$, Measured in the RT12055P-2100MHz test board amplifier circuit)



$P_{IN} = 0\text{dBm}$, $V_{DS} = 48\text{V}$, $I_{DQ} = 200\text{mA}$

Test Board Component Layout(2100MHz)

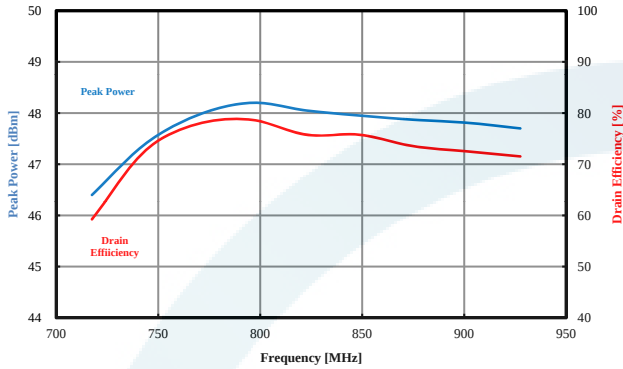


Part	Description	Part Number	Manufacturer
R1	51 ohm Chip Resistor	MCR10EZJH510	ROHM
L1	22nH Wire Wound Inductor	LQW18AN22NG00	MURATA
C1	12pF High Q Capacitor	201CHA120JSLE	TEMEX
C2	1.5pF High Q Capacitor	201CHA1R5BSLE	TEMEX
C3,C13	0.5pF High Q Capacitor	201CHA0R5BSLE	TEMEX
C4	Polymer Capacitor	TCJB476M010R0070	AVX
C5	1uF Chip Capacitor	GRM188R71H105KA01D	MURATA
C6	1nF Chip Capacitor	GRM188R71H102KA01D	MURATA
C7	10pF Chip Capacitor	GRM1885C1H100JA01D	MURATA
C8	56pF High Q Capacitor	201CHA560JSLE	TEMEX
C9,C14	-	-	-
C10	100pF High Q Capacitor	201CHA101JSLE	TEMEX
C11	0.1uF Chip Capacitor	C2012X7R2A104K	TDK
C12	10uF MLCC	RS80R2A106M	MARUWA
C15	3.3pF High Q Capacitor	201CHA3R3BSLE	TEMEX
C16	33uF Aluminum Capacitor	MKV100VCJ10M	SAMYOUNG
EMI1	1000pF EMI Filter	CTH32R102S20A	MARUWA
CON1	DC Connector	5268-06A	MOLEX
PCB	$\epsilon_r=3.66 \pm 0.05$, 0.030" (0.762mm)	RO4350B	ROGERS Corp.
TR1	55W GaN Transistor	RT12055P	RFHIC

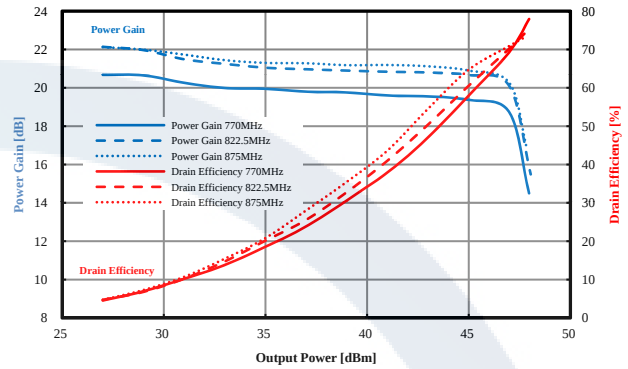
Typical Pulsed Signal Performance

(Tc=25°C, Measured in the RT12055P-800MHz test board amplifier circuit)

Peak Power, Drain Efficiency vs. Frequency



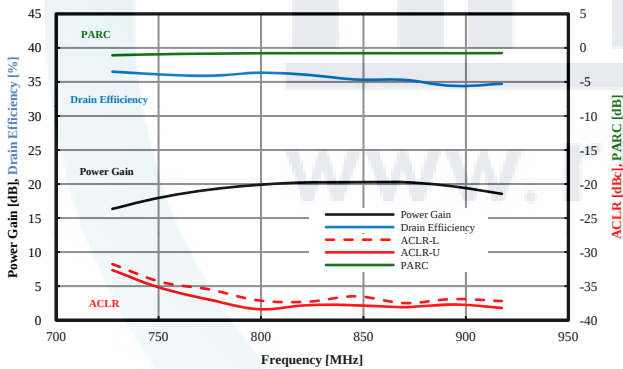
Pulsed Power Gain, Drain Efficiency vs. Output Power


 $V_{DS} = 48V, \quad I_{DQ} = 200mA, \text{ Pulse Width} = 100\mu\text{sec}, \text{ Duty Cycle} = 10\%$

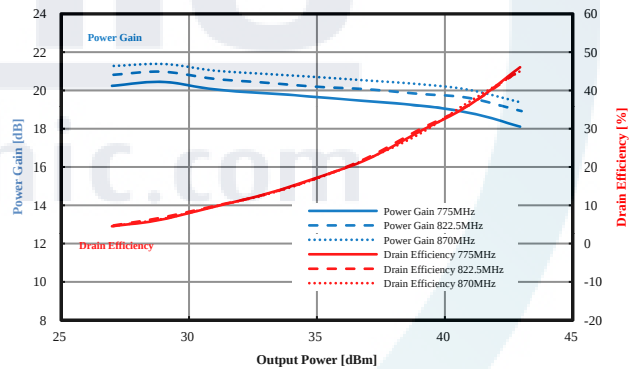
Typical LTE Signal Performance

(Tc=25°C, Measured in the RT12055P-800MHz test board amplifier circuit)

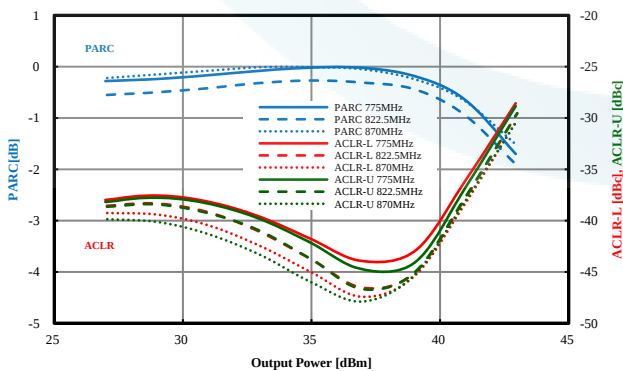
LTE Power Gain, Drain Efficiency, ACLR, PARC vs. Frequency



Power Gain, Drain Efficiency vs. Output Power



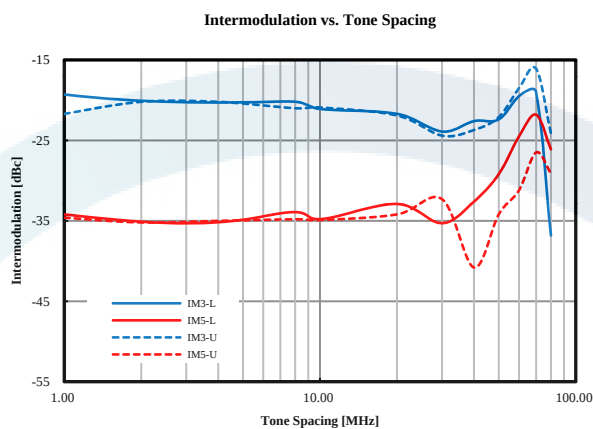
PARC, ACLR vs. Output Power


 $P_{AVG} = 41dBm, \quad V_{DS} = 48V, \quad I_{DQ} = 200mA$

LTE 10MHz BW, PAPR=7.5dB @ 0.01% Probability on CCDF

Typical 2-tone Intermodulation Imbalance Performance

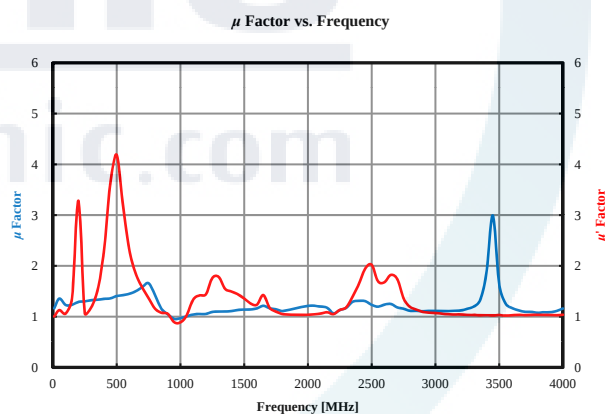
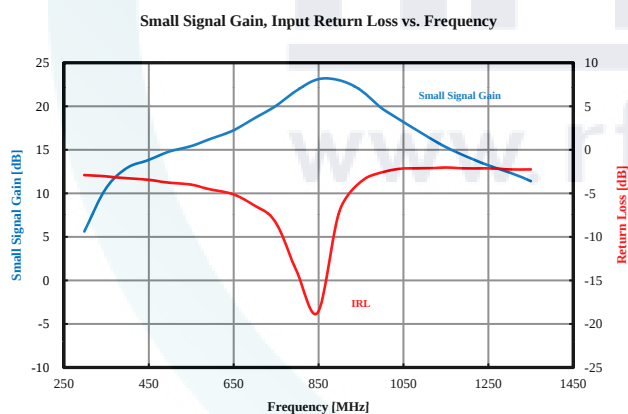
(Tc=25°C, Measured in the RT12055P-800MHz test board amplifier circuit)



2-tone Power = 45.4dBm, $V_{DS} = 48V$, $I_{DQ} = 200mA$

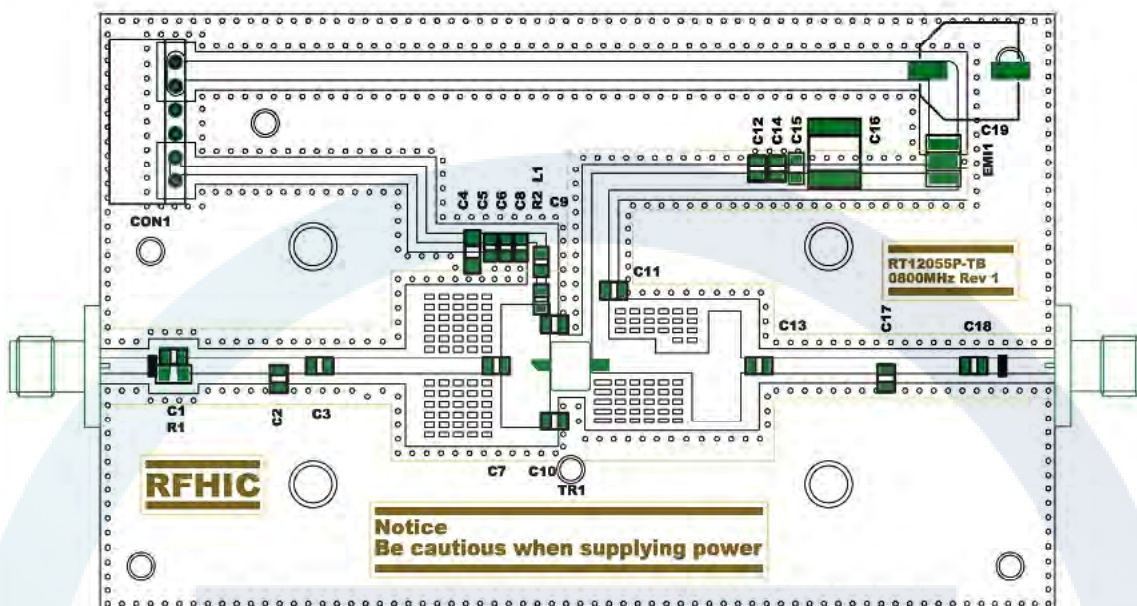
Typical Small Signal Performance

(Tc=25°C, Measured in the RT12055P-800MHz test board amplifier circuit)



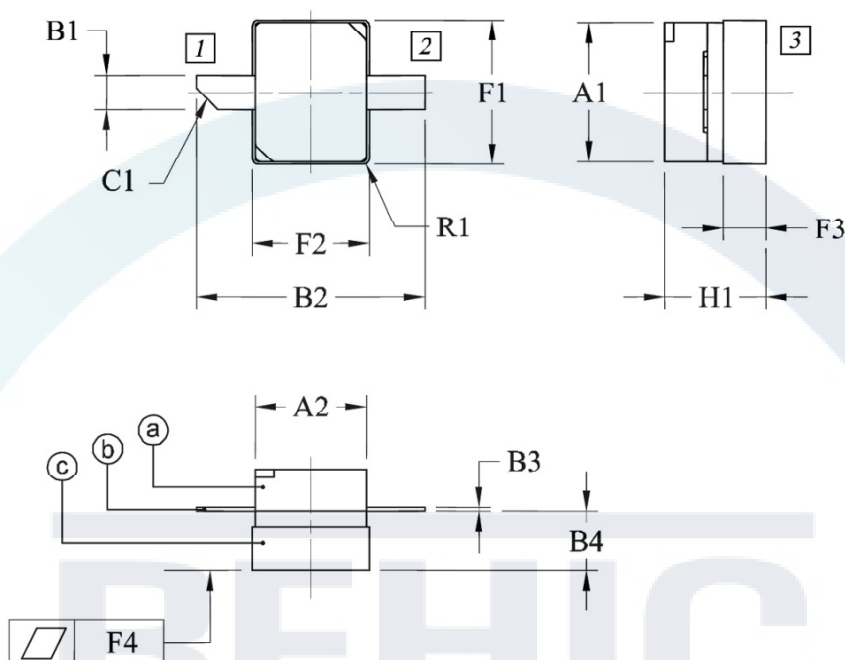
$P_{IN} = 0dBm$, $V_{DS} = 48V$, $I_{DQ} = 200mA$

Test Board Component Layout(800MHz)



Part	Description	Part Number	Manufacturer
R1	300 ohm Chip Resistor	MCR10EZJ301	ROHM
R2	51 ohm Chip Resistor	MCR10EZJ510	ROHM
L1	47nH Wire Wound Inductor	LQW18AN47NG00	MURATA
C1,C11	2.2pF High Q Capacitor	201CHA2R2BSLE	TEMEX
C2,C17	1.8pF High Q Capacitor	201CHA1R8BSLE	TEMEX
C3,C7,C12,C18	100pF High Q Capacitor	201CHA101JSLE	TEMEX
C4	Polymer Capacitor	TCJB476M010R0070	AVX
C5	1uF Chip Capacitor	GRM188R71H105KA01D	MURATA
C6	1nF Chip Capacitor	GRM188R71H102KA01D	MURATA
C8	10pF Chip Capacitor	GRM1885C1H100JA01D	MURATA
C9	1.5pF High Q Capacitor	201CHA1R5BSLE	TEMEX
C10	0.5pF High Q Capacitor	201CHA0R5BSLE	TEMEX
C13	56pF High Q Capacitor	201CHA560JSLE	TEMEX
C14	-	-	-
C15	0.1uF Chip Capacitor	C2012X7R2A104K	TDK
C16	10uF MLCC	RS80R2A106M	MARUWA
C19	33uF Aluminum Capacitor	MKV100VCJ10M	SAMYOUNG
EMI1	1000pF EMI Filter	CTH32R102S20A	MARUWA
CON1	DC Connector	5268-06A	MOLEX
PCB	$\epsilon_r=3.66 \pm 0.05, 0.030"$ (0.762mm)	RO4350B	ROGERS Corp.
TR1	55W GaN Transistor	RT12055P	RFHIC

Package Dimensions (Type: NS-CS01)



Pin Description	
Pin No	Function
1	Gate
2	Drain
3	Source

- (a) - Lid
 (b) - Lead Frame
 (c) - Flange

Dim.	INCH			MILLIMETER		
	MIN	TYP	MAX	MIN	TYP	MAX
A1	.189	.193	.197	4.80	4.90	5.00
A2	.150	.154	.157	3.80	3.90	4.00
B1	.042	.047	.052	1.07	1.20	1.33
B2	.295	.315	.335	7.50	8.00	8.50
B3	.004	.005	.007	0.10	0.13	0.18
B4	.076	.081	.086	1.93	2.05	2.18
C1 (Chamfer)	.026	.030	.033	0.65	0.75	0.85
F1	.196	.201	.206	4.97	5.10	5.23
F2	.156	.161	.167	3.97	4.10	4.23
F3	.054	.059	.064	1.37	1.50	1.63
F4	-	.001	-	-	0.03	-
H1	.132	.144	.152	3.35	3.65	3.85
L1	-	-	-	-	-	-
L2	-	-	-	-	-	-
R1 (Radius)	.004	.008	.012	0.10	0.20	0.30

Revision History

Part Number	Release Date	Version	Description	Data Sheet Status
RT12055P	January, 2016	1.1	Modified Thermal Resistance	-
RT12055P	February, 2015	1.0	Initial Release of Data sheet	-



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