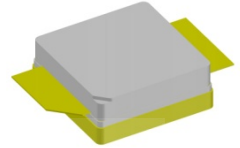


## Product Features

- 2500~2690MHz
- 110W Saturated Power @ 48V
- 72% Drain Efficiency @ Psat
- 39.6% Drain Efficiency @ 44dBm
- Internally Matched

## Applications

- WiMAX, LTE, WCDMA, GSM
- Multi-Band, Multi-Mode
- Multi-Carrier
- High Efficiency, Doherty Amplifier



Package Type : NS-AS01

Typical Single-Carrier LTE Performance ( $V_{DS} = +48V$ ,  $T_C = 25^\circ C$ ,  $50\Omega$ )

Frequency [MHz]	Peak Power		Average Power <sup>*1</sup>			
	Power [W]	Drain Efficiency [%]	Power [W]	Gain [dB]	Drain Efficiency [%]	ACLR [dBc]
2505.0	121.6	75.2	25	18.9	40.1	-29.2
2595.0	121.3	73.5	25	19.1	39.6	-27.7
2685.0	114.8	71.8	25	18.3	40.0	-27.9

## Note

\*1 Measured in the IE26110P test board amplifier circuit, under LTE 10MHz, PAR 7.5dB @0.01% probability on CCDF.

## Absolute Maximum Ratings

Rating	Symbol	Value	Unit	Condition
Drain to Source Voltage	$V_{DSS}$	150	V	$T_C = 25^\circ C$
Gate to Source Voltage	$V_{GS}$	-10, +2	V	$T_C = 25^\circ C$
Operating Voltage	$V_{DD}$	52	$V_{DC}$	-
Maximum Forward Gate Current	$I_{GMAX}$	16	mA	$T_C = 25^\circ C$
Maximum Drain Current <sup>*1</sup>	$I_{DMAX}$	6	A	$T_C = 25^\circ C$
Power Dissipation	$P_{DISS}$	55	W	$T_C = 85^\circ C$
Storage Temperature	$T_{STG}$	-65, +150	$^\circ C$	-
Case Operating Temperature	$T_C$	-40, +150	$^\circ C$	30 seconds
Operating Junction Temperature <sup>*2</sup>	$T_J$	225	$^\circ C$	-
Soldering Temperature <sup>*3</sup>	$T_S$	245	$^\circ C$	-

## Note

\*1 Current Limit for long term, reliable operation.

\*2 Continuous use at maximum temperature will affect MTTF.

\*3 Refer to the Application Note(AN-002) on soldering - "Solder Condition for RFHIC's GaN Device"

## Thermal Characteristics

Rating	Symbol	Value	Unit	Condition
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.55 <sup>*1</sup>	$^\circ C/W$	$T_C = 85^\circ C$

## Note

\*1 Measured for the IE26110P at dissipation power of 55W.

Electrical Characteristics ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Characteristics	Conditions	Symbol	Min	Typ	Max	Unit
<b>DC Characteristics</b> <sup>*1</sup>						
Gate Threshold Voltage	$V_{DS} = 10\text{V}$	$V_{GS(TH)}$	-3.8	-3.0	-2.3	$V_{DC}$
	$I_D = 14.4\text{mA}$					
Gate Quiescent Voltage	$V_{DS} = 48\text{V}$	$V_{GS(Q)}$	-	-3	-	$V_{DC}$
	$I_D = 500\text{mA}$					
Saturated Drain Current <sup>*2</sup>	$V_{DS} = 6\text{V}$	$I_{DS}$	12.0	14.4	-	A
	$V_{GS} = 2\text{V}$					
Drain-Source Breakdown Voltage	$V_{GS} = -8\text{V}$	$V_{BR}$	150	-	-	V
	$I_D = 14.4\text{mA}$					
Gate Leakage Current	$V_{GS} = -8\text{V}$	$I_{GLKG}$	-3.2	-	-	mA
	$V_{DS} = 120\text{V}$					
Drain Leakage Current	$V_{GS} = -8\text{V}$	$I_{DLKG}$	-	-	5.8	mA
	$V_{DS} = 120\text{V}$					
<b>RF Characteristics</b> ( $F_c=2595\text{MHz}$ unless otherwise noted)						
Saturated Output Power <sup>*3,6</sup>	$V_{DS} = 48\text{V}$	$P_{SAT}$	-	110	-	W
	$I_{DQ} = 500\text{mA}$					
Pulsed Drain Efficiency <sup>*3</sup>	$V_{DS} = 48\text{V}$	$\eta$	67	72	-	%
	$I_{DQ} = 500\text{mA}$					
	$P_{OUT} = P_{SAT}$ Pulsed					
Modulated Gain <sup>*4</sup>	$V_{DS} = 48\text{V}$	$G_{BR}$	18.0	19.0	-	dB
	$I_{DQ} = 500\text{mA}$					
	$P_{OUT} = 44\text{dBm}$					
LTE Linearity <sup>*4</sup>	$V_{DS} = 48\text{V}$	ACLR	-	-27.0	-25.0	dBc
	$I_{DQ} = 500\text{mA}$					
	$P_{OUT} = 44\text{dBm}$					
Modulated Drain Efficiency <sup>*4</sup>	$V_{DS} = 48\text{V}$	$\eta$	36.0	39.6	-	%
	$I_{DQ} = 500\text{mA}$					
	$P_{OUT} = 44\text{dBm}$					
Output Mismatch Stress <sup>*3,5</sup>	$V_{DS} = 48\text{V}$	VSWR	-	-	10:1	$\psi$
	$I_{DQ} = 500\text{mA}$					
	$P_{OUT} = P_{SAT}$ Pulsed					

## Note

\*1 Measured on wafer prior to packaging.

\*2 Scaled from PCM data.

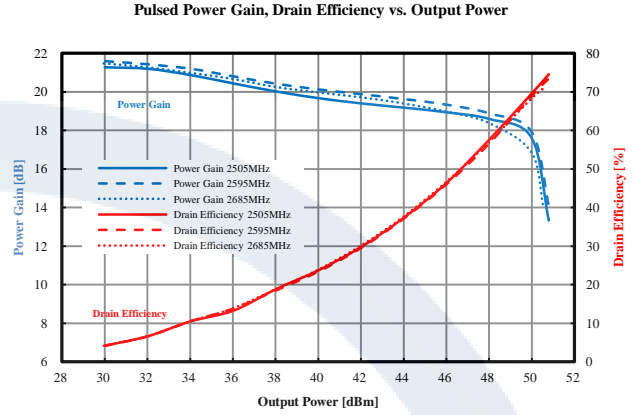
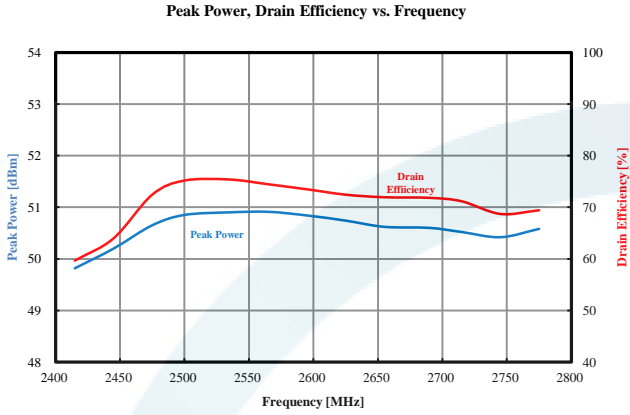
\*3 Pulse width 100 $\mu\text{sec}$ , Duty Cycle 10%.

\*4 Measured in the IE26110P test board amplifier circuit, under LTE 10MHz, PAR7.5dB @0.01% probability on CCDF.

\*5 Measured in the IE26110P test board amplifier circuit. No damage at all phase angles.

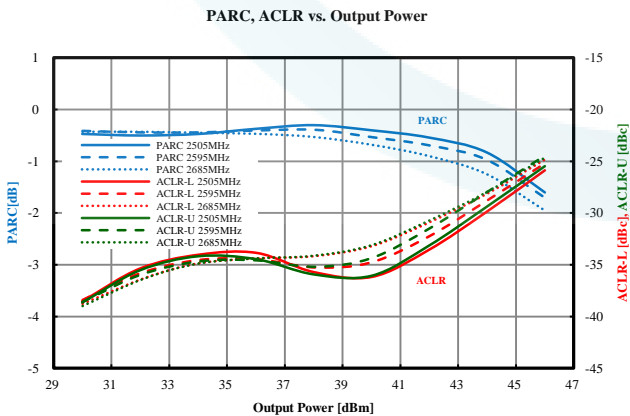
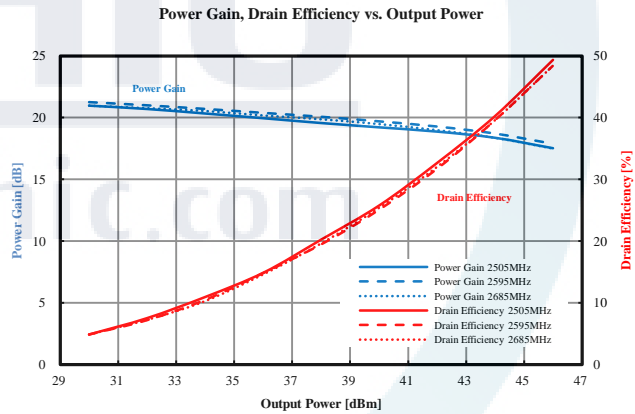
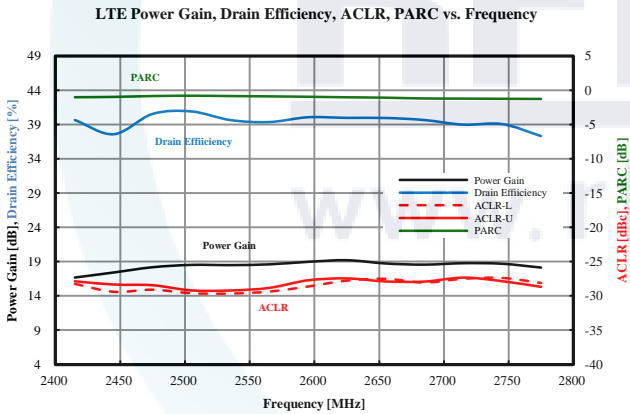
\*6 Psat is defined as  $\Delta P_{out}/\Delta P_{in} < 0.1$ , where  $\Delta P_{in}$  is increased input power,  $\Delta P_{out}$  is increased output power.

Typical Pulsed Signal Performance (Tc=25°C, Measured in the IE26110P test board amplifier circuit)



$V_{DS} = 48V$ ,  $I_{DQ} = 500mA$ , Pulse Width = 100µsec, Duty Cycle = 10%

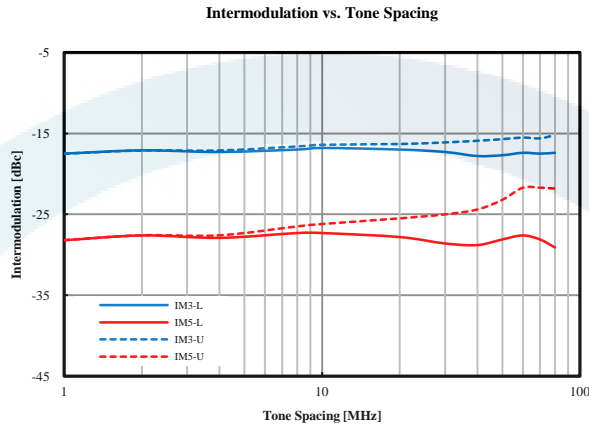
Typical LTE Signal Performance (Tc=25°C, Measured in the IE26110P test board amplifier circuit)



$P_{AVG} = 44dBm$ ,  $V_{DS} = 48V$ ,  $I_{DQ} = 500mA$   
 LTE 10MHz BW, PAPR=7.5dB @ 0.01% Probability on CCDF

Typical 2-tone Intermodulation Imbalance Performance

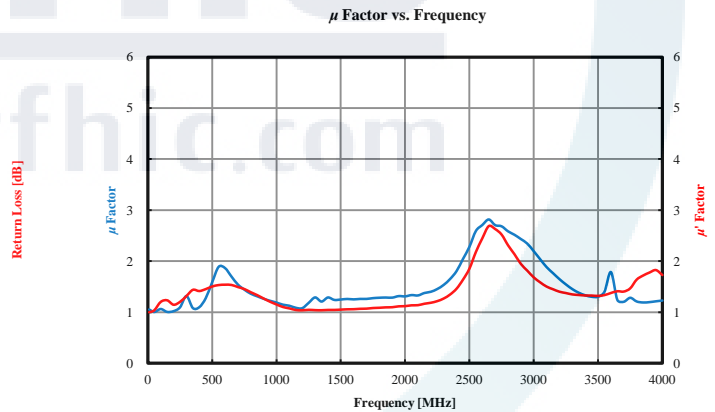
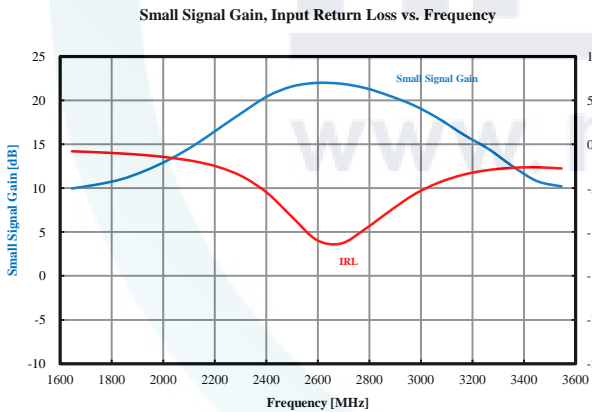
(Tc=25°C, Measured in the IE26110P test board amplifier circuit)



2-tone Power = 48.4dBm,  $V_{DS} = 48V$ ,  $I_{DQ} = 500mA$

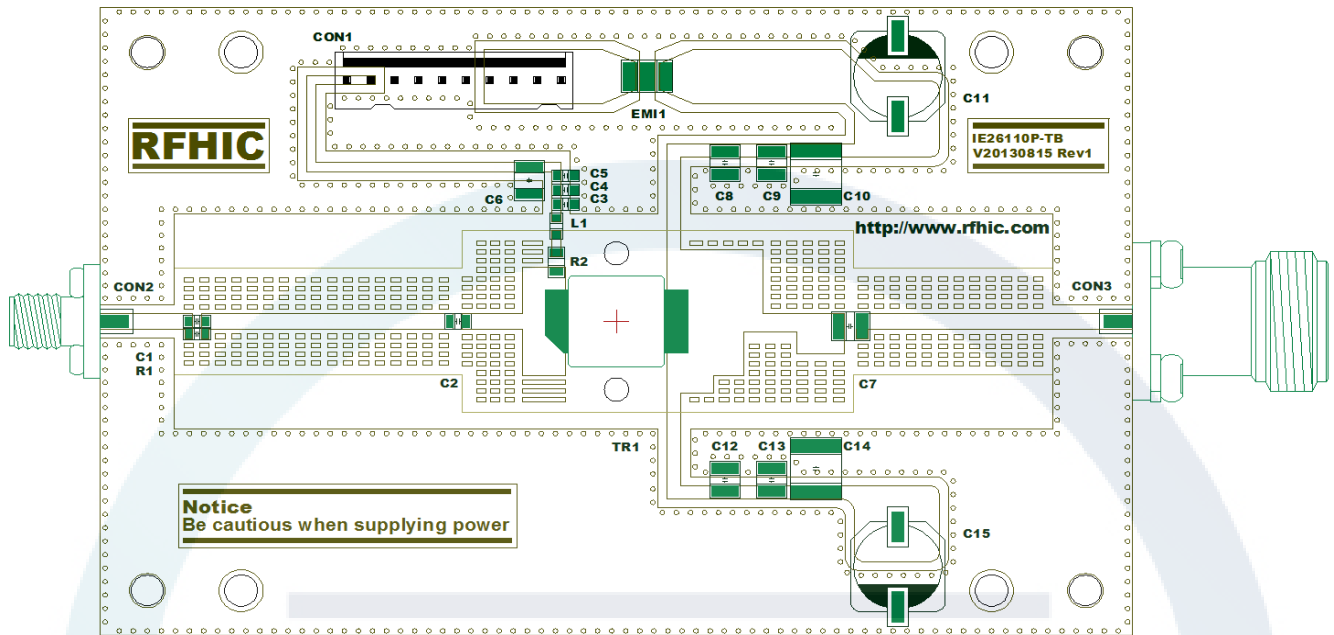
Typical Small Signal Performance

(Tc=25°C, Measured in the IE26110P test board amplifier circuit)



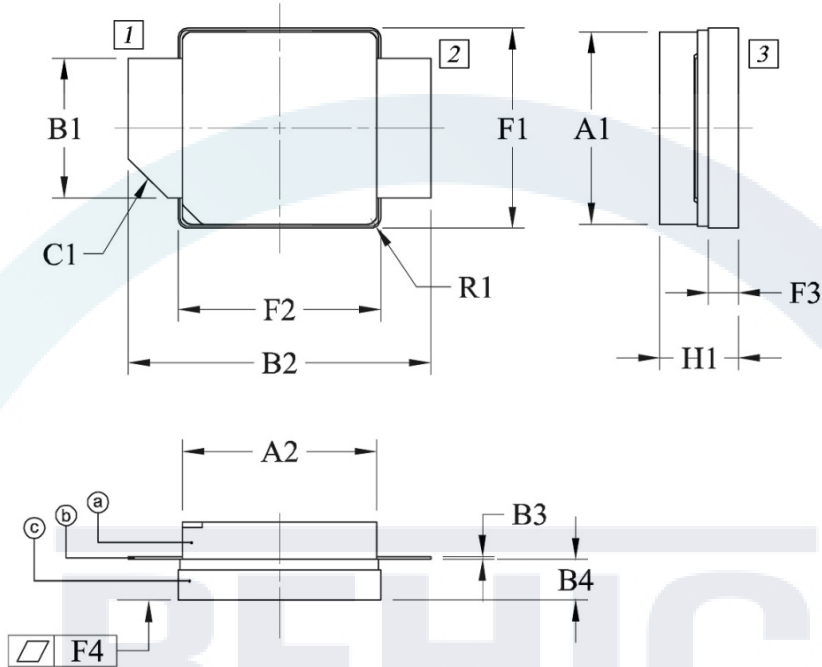
$P_{IN} = 0dBm$ ,  $V_{DS} = 48V$ ,  $I_{DQ} = 500mA$

## Test Board Component Layout



Part	Description	Part Number	Manufacturer
R1	10 ohm Chip Resistor	MCR03 EZPJ100	ROHM
R2	10 ohm Chip Resistor	MCR10 EZHJ100	ROHM
C1, C2	10pF High Q Capacitor	201CHA100JSLE	TEMEX
C3	10pF Chip Capacitor	GRM1885C1H100JA01D	MURATA
C4	1nF Chip Capacitor	GRM188R71H102KA01D	MURATA
C5	100nF Chip Capacitor	GRM188R71H104KA93D	MURATA
C6	Polymer Capacitor	TCJB476M010R0070	AVX
C7, C8, C12	10pF High Q Capacitor	501CHB100JSLE	TEMEX
C9, C13	100pF High Q Capacitor	501CHB101JSLE	TEMEX
C10, C14	10uF MLCC	RS80R2A106M	MARUWA
C11, C15	33uF Aluminum Capacitor	BDS100VC33MJ10TP	SAMYOUNG
L1	12nH Wire Wound Inductor	0805CS-120XJLC	COILCRAFT
EMI1	1000pF EMI Filter	CTH32R102S20A	MARUWA
CON1	DC Connector	LW0640	HANLIM
CON2	RF Connector	PAF-S02-000	GIGALANE
CON3	RF Connector	KCN8043110070	TELCON
PCB	$\epsilon_r=3.66 \pm 0.05, 0.030'' (0.762\text{mm})$	RO4350B	ROGERS Corp.
TR	110W GaN Transistor	IE26110P	RFHIC

Package Dimensions (Type: NS-AS01)



Pin Description	
Pin No	Function
1	Gate
2	Drain
3	Source

- Ⓐ- Lid
- Ⓑ- Lead Frame
- Ⓒ- Flange

Dim.	INCH			MILLIMETER		
	MIN	TYP	MAX	MIN	TYP	MAX
A1	.380	.384	.390	9.65	9.75	9.90
A2	.380	.384	.390	9.65	9.75	9.90
B1	.274	.280	.285	6.97	7.10	7.23
B2	.579	.598	.618	14.70	15.20	15.70
B3	.004	.005	.007	0.10	0.13	0.18
B4	.080	.085	.090	2.03	2.15	2.28
C1 (Chamfer)	.075	.079	.083	1.90	2.00	2.10
F1	.395	.400	.405	10.03	10.16	10.29
F2	.395	.400	.405	10.03	10.16	10.29
F3	.054	.059	.064	1.37	1.50	1.63
F4	-	.001	-	-	0.03	-
H1	.148	.159	.167	3.75	4.05	4.25
L1	-	-	-	-	-	-
L2	-	-	-	-	-	-
R1 (Radius)	.016	.020	.024	0.40	0.50	0.60

**Revision History**

Part Number	Release Date	Version	Description	Data Sheet Status
IE26110P	March, 2016	1.0	Modified Electrical Characteristics	-



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