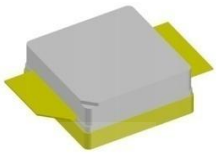
GaN Power Transistors IE21085P

Product Features

- 2110~2170MHz
- 85W Saturated Power @ 48V
- 75% Drain Efficiency @ Psat
- 42% Drain Efficiency @ 42.8dBm

Applications

- WiMAX, LTE, WCDMA, GSM
- Multi-Band, Multi-Mode
- Multi-Carrier
- High Efficiency, Doherty Amplifier



Package Type : NS-AS01

Typical Single-Carrier LTE Performance (V_{DS}= +48V, T_C=25℃, 50Ω)

Frequency [MHz]	Peak Power		Average Power*1			
	Power [W]	Drain Efficiency [%]	Power [W]	Gain [dB]	Drain Efficiency [%]	ACLR [dBc]
2115.0	88.3	78.3	19	20.6	43.1	-26.2
2140.0	89.5	78.0	19	20.7	43.1	-26.6
2165.0	88.7	75.4	19	20.5	42.7	-26.9

Note

*1 Measured in the IE21085P test board amplifier circuit, under LTE 10MHz, PAR 7.5dB @0.01% probability on CCDF.

Absolute Maximum Ratings

Rating	Symbol	Value	Unit	Condition
Drain to Source Voltage	V_{DSS}	150	V	Tc=25℃
Gate to Source Voltage	V_{GS}	-10, +2	V	Tc=25℃
Operating Voltage	V_{DD}	52	V_{DC}	-
Maximum Forward Gate Current	I_{GMAX}	12	mA	Tc=25℃
Maximum Drain Current*1	I_{DMAX}	4.5	A	Tc=25℃
Power Dissipation	P_{DISS}	47	W	Tc=85℃
Storage Temperature	T_{STG}	-65, +150	℃	-
Case Operating Temperature	T_C	-40, +150	℃	30 seconds
Operating Junction Temperature*2	T_J	225	℃	-
Soldering Temperature*3	T_S	245	℃	-

Note

*1 Current Limit for long term, reliable operation.

*2 Continuous use at maximum temperature will affect MTTF.

*3 Refer to the Application Note(AN-002) on soldering - "Solder Condition for RFHIC's GaN Device"

Thermal Characteristics

Rating	Symbol	Value	Unit	Condition
Thermal Resistance, Junction to Case	$R_{\theta JC}$	2.97 *1	℃/W	Tc=85℃

Note

*1 Measured for the IE21085P at dissipation power of 47W.

Electrical Characteristics (T_C=25℃ unless otherwise noted)

Characteristics	Conditions	Symbol	Min	Typ	Max	Unit
DC Characteristics[*1]						
Gate Threshold Voltage	**V_{DS} = 10V** **I_D = 10.8mA**	$V_{GS(TH)}$	-3.8	-3.0	-2.3	V_{DC}
Gate Quiescent Voltage	**V_{DS} = 48V** **I_D = 300mA**	$V_{GS(Q)}$	-	-2.6	-	V_{DC}
Saturated Drain Current[*2]	**V_{DS} = 6V** **V_{GS} = 2V**	I_{DS}	9.0	10.8	-	A
Drain-Source Breakdown Voltage	**V_{GS} = -8V** **I_D = 10.8mA**	V_{BR}	150	-	-	V
Gate Leakage Current	**V_{GS} = -8V** **V_{DS} = 120V**	I_{GLKG}	-2.4	-	-	mA
Drain Leakage Current	**V_{GS} = -8V** **V_{DS} = 120V**	I_{DLKG}	-	-	4.3	mA
RF Characteristics (Fc=2140MHz unless otherwise noted)						
Saturated Output Power[*3,6]	**V_{DS} = 48V** **I_{DQ} = 300mA**	P_{SAT}	-	85	-	W
Pulsed Drain Efficiency[*3]	**V_{DS} = 48V** **I_{DQ} = 300mA** **P_{OUT} = P_{SAT} Pulsed**	η	70	75	-	%
Modulated Gain[*4]	**V_{DS} = 48V** **I_{DQ} = 300mA** **P_{OUT} = 42.8dBm**	G_{BR}	19.0	20.0	-	dB
LTE Linearity[*4]	**V_{DS} = 48V** **I_{DQ} = 300mA** **P_{OUT} = 42.8dBm**	ACLR	-	-26.0	-23.0	dBc
Modulated Drain Efficiency[*4]	**V_{DS} = 48V** **I_{DQ} = 300mA** **P_{OUT} = 42.8dBm**	η	38.0	42.0	-	%
Output Mismatch Stress[*3, 5]	**V_{DS} = 48V** **I_{DQ} = 300mA** **P_{OUT} = P_{SAT} Pulsed**	VSWR	-	-	10:1	ψ

Note

*1 Measured on wafer prior to packaging.

*2 Scaled from PCM data.

*3 Pulse width 100μsec, Duty Cycle 10%.

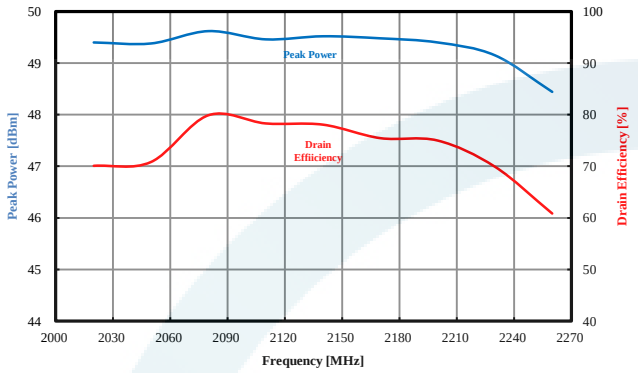
*4 Measured in the IE21085P test board amplifier circuit, under LTE 10MHz, PAR7.5dB @0.01% probability on CCDF.

*5 Measured in the IE21085P test board amplifier circuit. No damage at all phase angles.

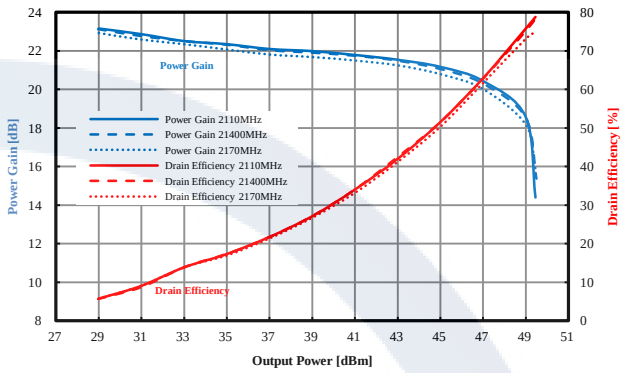
*6 Psat is defined as ΔPout/ΔPin<0.1, where ΔPin is increased input power, ΔPout is increased output power.

Typical Pulsed Signal Performance (Tc=25°C, Measured in the IE21085P test board amplifier circuit)

Peak Power, Drain Efficiency vs. Frequency



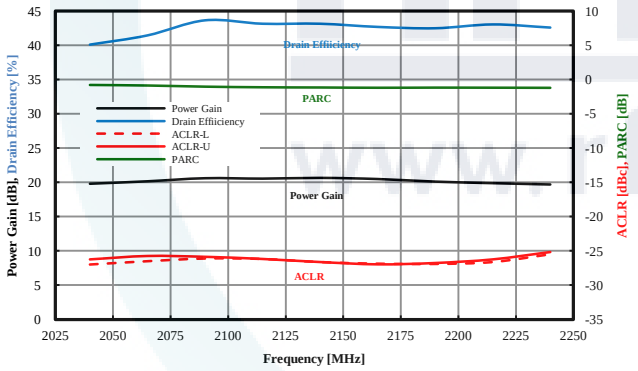
Pulsed Power Gain, Drain Efficiency vs. Output Power



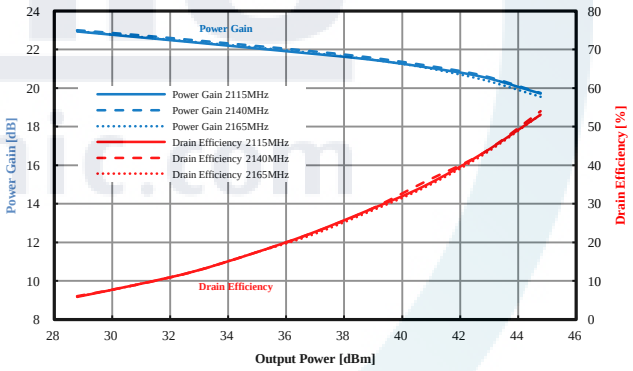
V_{DS} = 48V, I_{DQ} = 300mA, Pulse Width = 100µsec, Duty Cycle = 10%

Typical LTE Signal Performance (Tc=25°C, Measured in the IE21085P test board amplifier circuit)

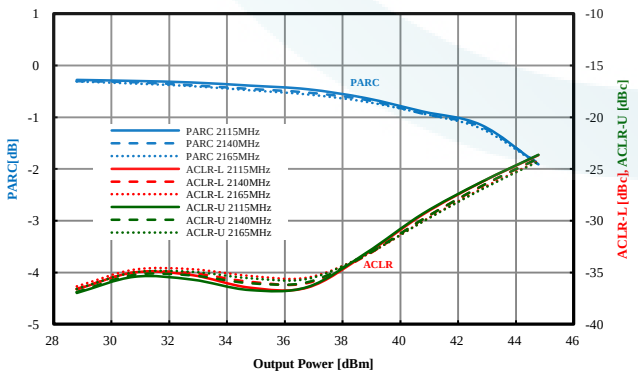
LTE Power Gain, Drain Efficiency, ACLR, PARC vs. Frequency



Power Gain, Drain Efficiency vs. Output Power



PARC, ACLR vs. Output Power

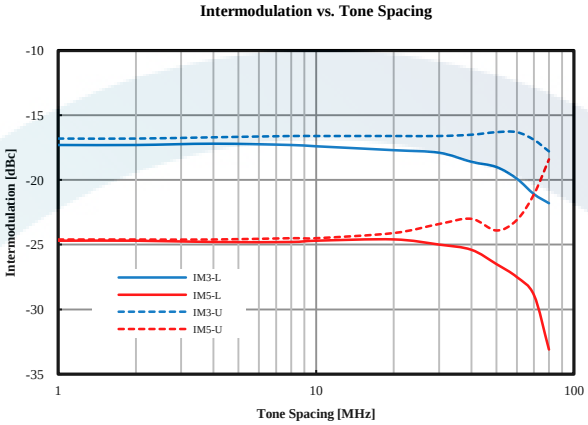


P_{AVG} = 42.8dBm, V_{DS} = 48V, I_{DQ} = 300mA

LTE 10MHz BW, PAPR=7.5dB @ 0.01% Probability on CCDF

Typical 2-tone Intermodulation Imbalance Performance

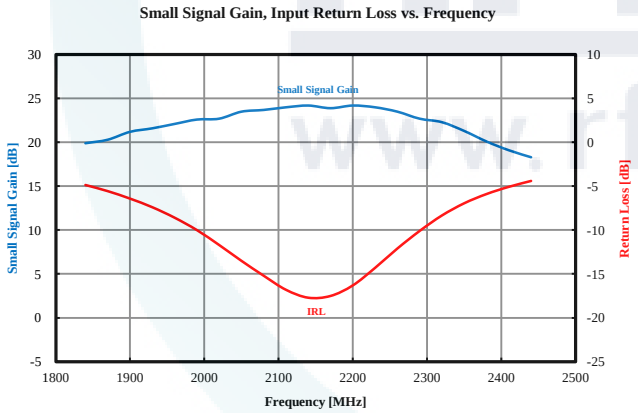
(Tc=25℃, Measured in the IE21085P test board amplifier circuit)

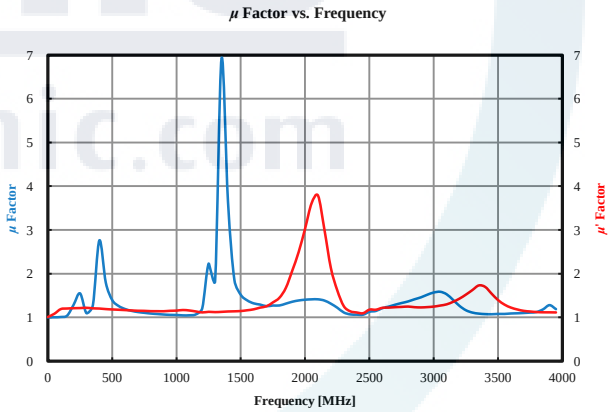


2-tone Power = 47.3dBm, V_{DS} = 48V, I_{DQ} = 300mA

Typical Small Signal Performance

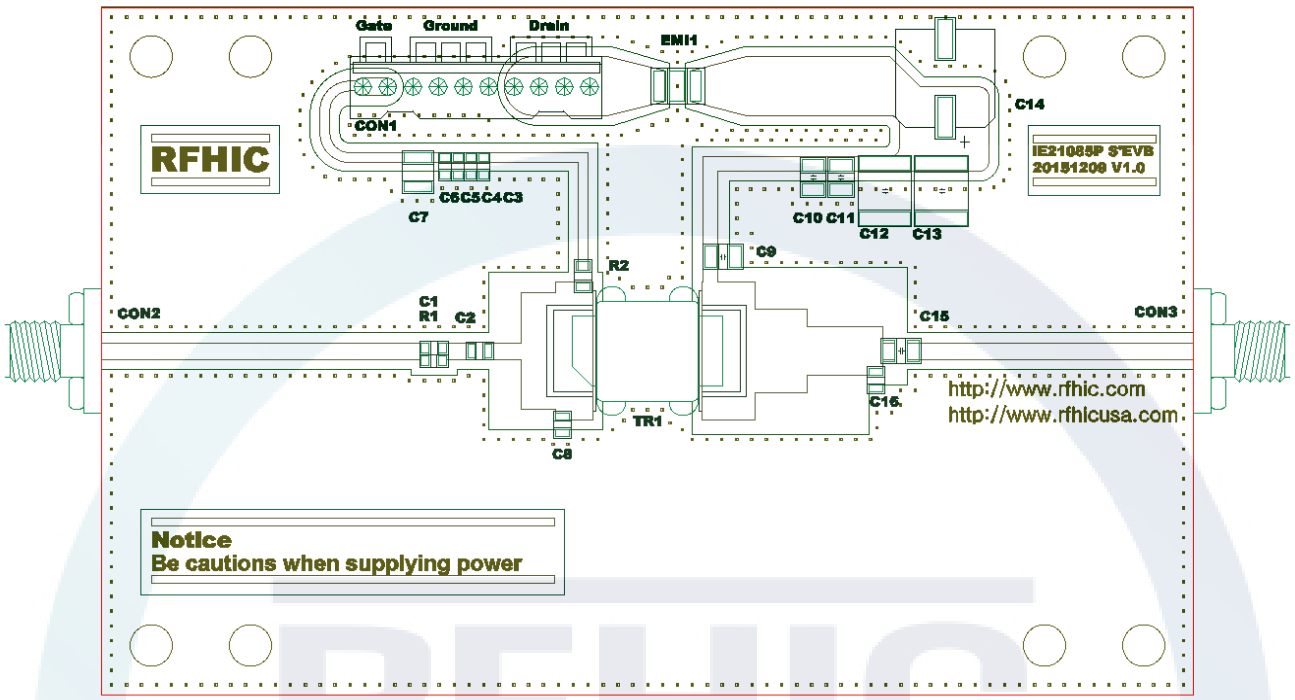
(Tc=25℃, Measured in the IE21085P test board amplifier circuit)





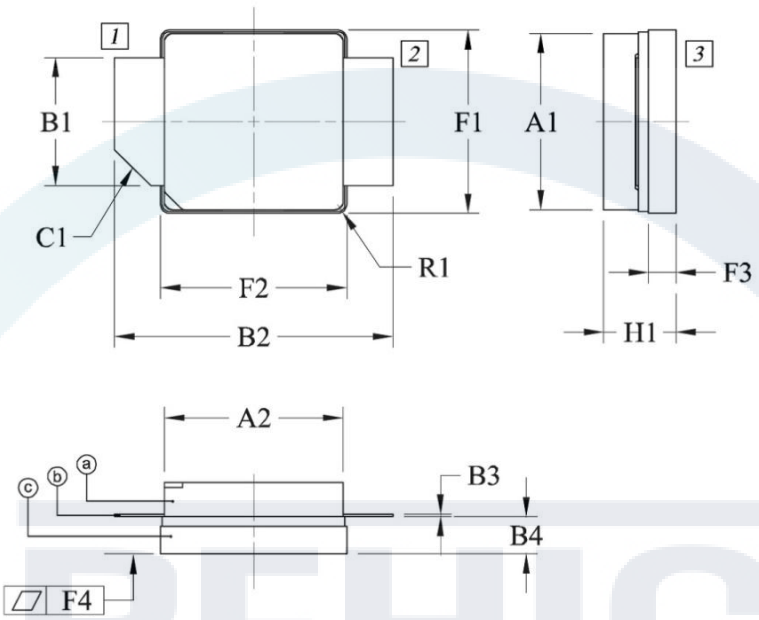
P_{IN} = 0dBm, V_{DS} = 48V, I_{DQ} = 300mA

Test Board Component Layout



Part	Description	Part Number	Manufacturer
R1	10ohm Chip Resistor	MCR03EZPJ100	ROHM
R2	100ohm Chip Resistor	MCR03EZPJ101	ROHM
C1, C2	10pF High Q Capacitor	201CHA100JSLE	TEMEX
C3	10pF Chip Capacitor	GRM1885C1H100JA01D	MURATA
C4	1nF Chip Capacitor	GRM188R71H102KA01D	MURATA
C5	100nF Chip Capacitor	GRM188R71C104KA01D	MURATA
C6, C8	-	-	-
C7	47uF Polymer Capacitor	TCJB476M010R0070	AVX
C9, C10, C15	10pF High Q Capacitor	501CHB100JSLE	TEMEX
C11	100pF High Q Capacitor	501CHB101JSLE	TEMEX
C12, C13	22uF MLCC	RS80R2A226M-TPNT	MARUWA
C14	33uF Aluminum Capacitor	BDS100VC33MJ10TP	SAMYOUNG
C16	0.9pF High Q Capacitor	201CHA0R9BSLE	TEMEX
EMI1	1000pF EMI Filter	CTH32R102S20A-TM	MARUWA
CON1	DC Connector	LW0640-10	HANLIM
CON2, CON3	RF Connector	SMA-4Hole	ANY
PCB	εr=3.66 ± 0.05, 0.030” (0.762mm)	RO4350B	ROGERS
TR	85W GaN Transistor	IE21085P	RFHIC

Package Dimensions (Type:NS-AS01)



Pin Description	
Pin No	**Function**
1	Gate
2	Drain
3	Source

ⓐ- Lid
ⓑ- Lead Frame
ⓒ- Flange

Dim.	INCH			MILLIMETER		
	MIN	TYP	MAX	MIN	TYP	MAX
A1	.380	.384	.390	9.65	9.75	9.90
A2	.380	.384	.390	9.65	9.75	9.90
B1	.274	.280	.285	6.97	7.10	7.23
B2	.579	.598	.618	14.7	15.2	15.7
B3	.004	.005	.007	0.10	0.13	0.18
B4	.080	.085	.090	2.03	2.15	2.28
C1 (Chamfer)	.075	.079	.083	1.90	2.00	2.10
F1	.395	.400	.405	10.03	10.16	10.29
F2	.395	.400	.405	10.03	10.16	10.29
F3	.054	.059	.064	1.37	1.50	1.63
F4	-	.001	-	-	0.03	-
H1	.148	.159	.167	3.75	4.05	4.25
L1	-	-	-	-	-	-
L2	-	-	-	-	-	-
R1 (Radius)	.016	.020	.024	0.40	0.50	0.60

Revision History

Part Number	Release Date	Version	Description	Data Sheet Status
IE21085P	October, 2018	1.1	Modified Thermal Characteristics	-
IE21085P	March, 2016	1.0	Modified Electrical Characteristics	-